REMARKS

In the present Office action: paragraph [0001] of the specification was objected to; claims 1-20 were provisionally rejected on the grounds of nonstatutory obviousness-type double patenting over claims 1-41 of U.S. Patent Application Serial No. 10/626,063; claims 1, 4-6, 9, 12-16, 19 and 20 were rejected under 35 U.S.C. §102(b) as being anticipated by U.S. Patent No. 5,793,817 (hereinafter "Wilson"); claims 2, 7, 10, 11, 17 and 18 were rejected under 35 U.S.C. §103(a) as being unpatentable over Wilson in view of U.S. Patent No. 5,033,110 (hereinafter "Harmon"); and claims 3 and 8 were objected to as being dependent upon a rejected base claim, but were indicated to be allowable if rewritten in independent form.

Applicant has amended paragraph [0001] of the specification to address the Office action objection and has made a minor correction to paragraph [0038] of the specification. Applicant acknowledges the provisional rejections of claims 1-20 on the grounds of nonstatutory obviousness-type double patenting over claims 1-41 of U.S. Patent Application Serial No. 10/626,063 and will, when the rejection is made non-provisional, address the rejections. Applicant extends appreciation to the Examiner for the indication of allowable subject matter. Applicant has amended claims 1, 6, 8, 9, 14 and 15 and cancelled claims 3, 19 and 20. More specifically, Applicant has amended independent claims 1, 9 and 15 in a manner which Applicant believes conforms to the subject matter indicated to be allowable.

Moreover, Applicant notes that while Wilson discloses a DC offset circuit that subtracts a DC offset from a loop error signal, the DC offset is measured at an input of an upconverter core. While the DC offset circuit is effectively removed during an operate period and a storage unit provides the DC offset to a differential amplifier that subtracts the DC offset from the loop error signal during the operate period, the Wilson storage element does not provide an offset correction signal that is representative of power of an output signal at a carrier frequency at an output terminal of an upconverter core. More specifically, with reference to Wilson Fig. 3, switch 76 is open when switch 42 is closed. As is disclosed, the Wilson DC offset circuit attempts to compensate for local oscillator feedthrough and DC offsets at the output of mixer 56 so that the main feedback loop does not compensate for the DC offset at the output of mixer 56 and produce an unwanted carrier signal at the output of RF amplifier 38 (see col. 5, lines 25-35). While of different scope, independent claims 1, 9 and 15 are allowable over the applied prior art

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for at least the reasons set forth above. Additionally, claims 2, 4-8, 10-14 and 16-18 are also allowable for at least the reason that the claims depend upon an allowable claim.

CONCLUSION

Therefore Applicant respectfully submits that the present application is now in condition for allowance. If for any reason the Examiner deems the application is not in condition for allowance after entry of this amendment and/or consideration of the accompanying Remarks, Applicant requests the Examiner to contact the undersigned attorney at the telephone number listed below to advance the prosecution of this application.

The Commissioner is hereby authorized to charge any fees that may be required, or credit any overpayment, to Deposit Account Number 50-3797.

Respectfully submitted,

_09-29-06_
Date

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